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U.S. UTILITY Patent Application

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PATENT NUMBER and  
ISSUE DATE

APPL NUM 10006924	FILING DATE 12/04/2001	CLASS 514	SUBCLASS 327	GAU 2817	EXAMINER JACQUES WILLIOM
<b>**APPLICANTS:</b> Stockstad Troy; 330/255 Nguyen Khanh 07/515961 2/29/2000 6,353,363					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CON OF 09/516,961 03/01/2000 PAT 6,316,469 * (*)Data provided by applicant is not consistent with PTO records.					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 60178-300402	
TITLE : Low voltage rail-to-rail CMOS output stage					
U.S. DEPT. OF COMM./PAT. & TM-PTO-438L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for 0.0
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawn	Fig. Drawn
		Printed	Fig. Drawn
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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